

Searching PAJ

Page 1 of 2

# PATENT ABSTRACTS OF JAPAN

(11)Publication number : 04-313996  
 (43)Date of publication of application : 05.11.1992

(51)Int.Cl.

H04R 3/12

(21)Application number : 03-004842  
 (22)Date of filing : 21.01.1991

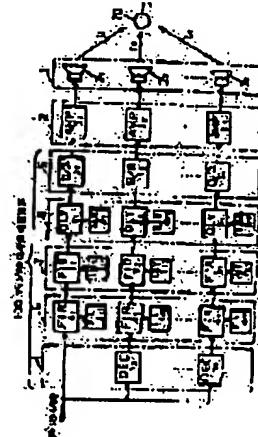
(71)Applicant : MITSUBISHI ELECTRIC CORP  
 (72)Inventor : OKINO NOBORU  
 OSUGA YOSHIHARU  
 YASHIMA NOBORU

## (54) MULTI-AMPLIFIER SYSTEM SPEAKER SYSTEM

### (57)Abstract:

**PURPOSE:** To obtain the multi-amplifier system speaker system realizing high fidelity acoustic reproduction by realizing a flat sound pressure characteristic of a radiation sound from each speaker and a linear phase, eliminating a phase difference and a delay time difference from each radiation so as to make the overall sound pressure characteristic flat.

**CONSTITUTION:** Each channel is provided with a decimation circuit 1 to decrease a sampling frequency of a digital input signal, a frequency band split circuit 4, a characteristic correction use inverse filter to realize a flat sound pressure characteristic and linear phase characteristic of a speaker unit 3, a delay time correction circuit 8, a D/A converter circuit 5 converting a digital signal into an analog signal, and a voice signal processing circuit connecting a power amplifier 2 and the speaker unit 3 in cascade and each channel is configurated in parallel. In the case of an analog voice input signal, an A/D converter and an overall sound pressure characteristic correction circuit are provided to a pre-stage of the decimation circuit.



### LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

9  
 1.0 アンチ・エイリアシング・フィルタ  
 1.1 D/A変換回路  
 1.2 総合特性補正用直線位相形FIRフィルタ  
 1.3 総合特性補正用低価発生回路  
 4.7. 带域分割/選フィルタ用FIRフィルタ

47.1 対称分割／逆フィルタ用係数発生回路  
 47.8 対称分割／逆フィルタ／連延給正用 FIR フィルタ  
 47.8.1 対称分割／逆フィルタ／連延給正用係数発生回路

11

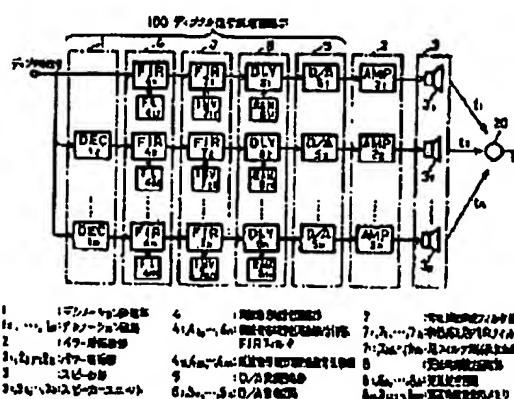
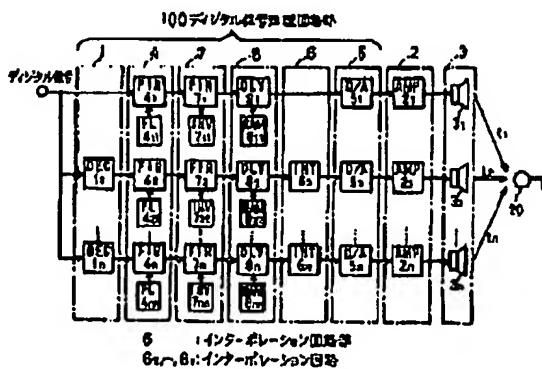
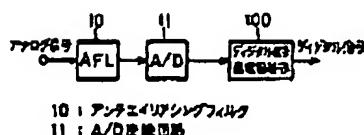


图21

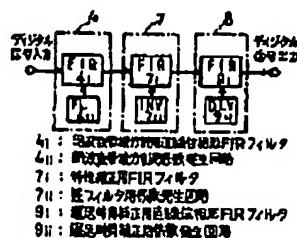


108

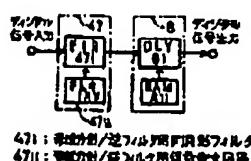


44-83424-313996

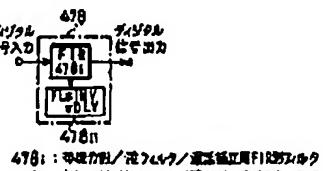
16



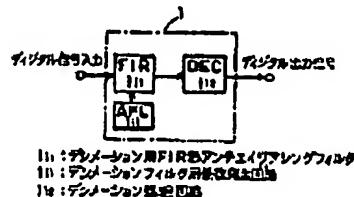
[图4-1]



152



【图8】



(7)

林國平4-313996

〔圖 7〕

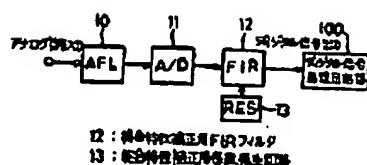
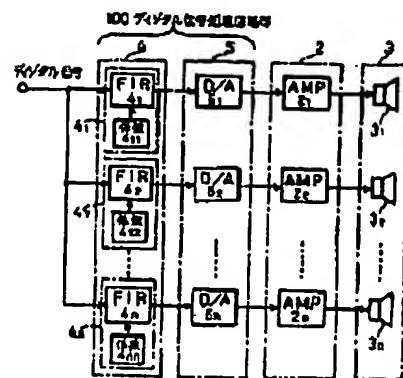


图8



《平定回疆方略》

〔提出日〕平成4年5月12日

藏山石 千岁

### 〔修正對金書報名〕假想書

【清正好景斋画名】明顾

### 【纠正方法】追加

[修正內容]

〔図9〕 種差別のブロック回路図である